



POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee, Estd: 1906
(Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

Raghavareddy Street, Kothapeta, Vijayawada-520001.

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Faculty Profile

T. SIREESHA,

D.No. 4-28-1/6,

Pendem Ramulu street,

Chittinagar, Vijayawada – 520 001.

Mobile: 9502080080

Email: sirishatamma@gmail.com/ sirishatamma@pscmr.ac.in



Experience:

Teaching: 11 years

Industry: 1 year

Career Profile

| Year | Organization | Role |
|----------------|---|------------------|
| 2015-Till date | Potti Sriramulu Chalavadi Mallikharjuna Rao College of Engineering and Technology, Vijayawada | Asst. Professor |
| 2014-2015 | Research Centre Imarath, RCI – DRDO, Hyderabad | Research Scholar |
| 2005-2013 | K.B.N. College | Lecturer |

Qualification

| Degree | Board / University | Date of Completion | % Marks |
|---------------------------------|--|--------------------|---------|
| Ph.D. (Pursuing) in ELECTRONICS | ANU-Registered in 2013 | - | - |
| M. Tech. | KL University | 2015 | 95 |
| M. Phil. | ANU | 2012 | A-Grade |
| M. Sc. | P.B. Siddhartha Arts & Science College | 2005 | 84 |
| B. Sc. | Sarada College | 2003 | 71 |
| Intermediate | Board of Intermediate Education | 2000 | 59.9 |
| SSC | Board of Secondary School of Education | 1998 | 68 |

The subjects handled:

1. Electronic Devices and Circuits
2. Basic Electronic Devices
3. Linear Integrated Circuit Applications
4. Microprocessors and Microcontrollers
5. Real Time Operating Systems

Achievements

| Certification |
|---|
| 1. Received Gold Medal and Merit Certificate in M.Tech. Embedded Systems during the year 2013-2015 from KL University on 23/01/2016. |
| 2. Received an Academic I Prize in M.Tech. Embedded Systems during the year 2013-2014 from KL University. |
| 3. Received Gold Medal in M.Sc. Electronics during the year 2003-2005 from P.B.Siddhartha College of Arts & Science. |
| 4. Got University First Rank Certificate in M.Sc. Electronics from ACHARYA NAGARJUNA UNIVERSITY. |

Certifications

Curriculum

| Certification | Date Certified |
|--|-----------------------|
| 1. Received a Govt. License and Certificate on "HAM RADIO", from Amateur Radio, Vijayawada | December 2016 |
| 2. Received a Task based Training (TBT-2016) Certificate on "Teacher Training Through the e-Yantra Lab Setup Initiative" from IIT Bombay | June 2016 |

Extra Curriculum

| Certification Details |
|---|
| 3. Certificate of "Winners in TENNI COIT" in 10 th Annual day Celebrations held on March 2018. |
| 4. Certificate of "Third Prize in SHOT PUT" in 10 th Annual day Celebrations held on March 2018. |
| 5. Certificate of "Runners in CARROMS" in 10 th Annual day Celebrations held on March 2018. |
| 6. Certificate of "Runners in THROW BALL" in 10 th Annual day Celebrations held on March 2018. |
| 7. Certificate of "Winners" in Rangoli Competeion held on January 2016. |
| 8. Certificate of "Winners in CHESS" in 8 th Annual day Celebrations held on March 2016. |
| 9. Certificate of "Third Prize in SHOT PUT" in 8 th Annual day Celebrations held on March 2016. |
| 10. Certificate of "Runners in TENNI COIT" in 8 th Annual day Celebrations held on March 2016. |
| 11. Certificate of "Runners in THROW BALL" in 8 th Annual day Celebrations held on March 2016. |

Affiliation to Professional Bodies

1. IEEE Membership
2. IAENG Membership
3. ISSE Life Membership

Publications

1. T. Sireesha and Dr. K. Krishna Murthy published a paper entitled with **"Comparative Analysis of Gyro-Parameters in Digital Closed-Loop Interferometric Fibre-Optic-Gyro based on Variations in V2n Ramp and Vn/2 Bias Voltages"** in WSEAS Transactions on System & Control, August 2018, E-ISSN: 2224-2856, Vol.13, Page. No. 353-363.
2. T. Sireesha, M.N.L Kalyani and D.Gowthami published a paper entitled with **"Design of an Autonomous Vehicle for Precision Agriculture using Sensor Technology"** in WSEAS Transactions on Environment & Development, March 2018, E-ISSN: 2224-3496, Vol.14, Page. No. 153-160.
3. T. Sireesha and Dr. K. Krishna Murthy published a paper entitled with **"Analysis of Dead Zone Compensation in Digital Closed-Loop IFOG Using MATLAB"** in Journal of Advanced Research in Dynamical and Control Systems (JARDCS), July 2017, ISSN 1943-023X, Vol.11, Special Issue, Page. No. 367-373.
4. T. Sireesha and K. Sudhakar published a paper entitled with **"Multi-Vehicle-Tracking-Locking-and-Speed-Control-System-Based-on-GPS-and-GPS-Module"** in International Journal Of Computer Science Mechatronics (IJCSM), June 2016, ISSN: 2455-1910, Vol. 2, Issue. No. 2, Page. No. 19-26.
5. T. Sireesha and K. Sudhakar published a paper entitled with **"Way-Finding-Electronic-Server-par-Visually-impaired-people"** in International Journal Of Computer Science Mechatronics (IJCSM), June 2016, ISSN: 2455-1910, Vol. 2, Issue. No. 2, Page. No. 34-39.
6. T. Sireesha and Dr. K. Krishna Murthy published a paper entitled with **"Comparative Assessment on Linearity Test Based V2n and Vn/2 Voltage variations of closed Loop IFOG"** in International Journal of Electrical and Computer Engineering (IJECE), April 2016, ISSN: 2088-8708, DOI: 10.11591/ijece.v6i2.8809, Vol. 6, Issue. No. 2, Page. No. 583-595.
7. T. Sireesha and Dr. K. Sreenivasa Ravi published a paper entitled with **"Comparative Assessment on Ramp and Bias Voltage variations of closed Loop Interferometric Fiber Optic Gyroscope"** in Indian Journal of Science and Technology (IJST), August 2015, ISSN(Print): 0974-6846 and ISSN(Online): 0974-5645, DOI: 10.17485/ijst/2015/ v8i18/67723, Vol. 8, Issue. No.18, Page. No. 1-11.
8. T. Sireesha and Dr. K. Sreenivasa Ravi published a paper entitled with **"Comparative Assessment on Ramp and Bias Voltage Variations of closed Loop Interferometric Fiber Optic Gyroscope"** in SSRG International Journal of Computer Science and Engineering (SSRG IJCSE), Special Issue, May 2015, ISSN: 2348-8387, Page. No. 229-237.
9. T. Sireesha and Dr. K. Sreenivasa Ravi published a paper entitled with **"Design of an Autonomous Vehicle for Precision Agriculture using Sensor Technology"** in SSRG International Journal of Computer Science and Engineering (SSRG IJCSE), Special Issue, May 2015, ISSN: 2348-8387, Page. No. 329-335.
10. B. Naresh Kumar Reddy, N. Venktram and T. Sireesha published a paper entitled with **"An Efficient Data Transmission by using Modern USB Flash Drive"** in International Journal of Multimedia and Ubiquitous Engineering (IJMUE), December 2014, ISSN: 1975-0080, DOI: 10.14257/ijmue.2014.9.10.26, Vol. 9, Issue. No.10, Page. No. 271-282.
11. B. Naresh Kumar Reddy, N. Venktram and T. Sireesha published a paper entitled with **"An Efficient Data Transmission by using Modern USB Flash Drive"** in International Journal of Electrical and Computer Engineering (IJECE), October 2014,

ISSN: 2088-8708, DOI: 10.11591/ijece.v6i2.8809, Vol. 4, Issue. No.5, Page. No. 730-740.

12. M. S. V. Vara Prasad, T. Sireesha and Dr. K. Krishna Murthy published a paper entitled with "**A Numerical Simulation of Optical Receiver Performance of WDM Signals in a PON**" in STM Journals- Recent Trends in Electronics and Communication Systems (STM Journals- RTECS), March 2014, ISSN: 2393-8757, Vol. 1, Issue. No.1, Page. No. 21-24.
13. M. S. V. Vara Prasad, Dr. K. Krishna Murthy and T. Sireesha published a paper entitled with "**Optical Receiver Performance of a WDM PON using PSO for the Decision Threshold Control Method**" International Journal of Engineering Associates (IJEAA), August 2013, ISSN: 2320-0804, Vol. 2, Issue. No.4, Page. No. 48-51.

Books

1. Published a Text Book on "Electronics Engineering Technician (EET) Paper-I" Inter Vocational Course, BIEAP - Board of Intermediate Education, Andhra Pradesh.

Conferences, Seminars, Workshops & Guest Lectures Attended

Conferences

1. T. Sireesha, M.N.L Kalyani and D.Gowthami published a paper entitled with "**Wireless Energy Transfer**" in 2nd International Conference on Contemporary Engineering And Technology (ICCET-2018) held on April 2nd to April 3rd 2018, PSCMRCET, Vijayawada.
2. U. Bhagya Sree and T. Sireesha, published a paper entitled with "**Sustainable Water Management System for Multiple Cropping based on IOT**" in 2nd International Conference on Contemporary Engineering And Technology (ICCET-2018) held on April 2nd to April 3rd 2018, PSCMRCET, Vijayawada.
3. T. Sireesha and Dr. K. Krishna Murthy published a paper entitled with "**Analysis of Dead Zone Compensation in Digital Closed-Loop IFOG Using MATLAB**" in 1st International Conference on Contemporary Engineering And Technology (ICCET-2017) held on March 31st to April 1st 2017, PSCMRCET, Vijayawada.
4. B. Pujitha and T. Sireesha, published a paper entitled with "**WITRICITY based on Embedded Systems**" in 1st International Conference on Contemporary Engineering And Technology (ICCET-2017) held on March 31st to April 1st 2017, PSCMRCET, Vijayawada.
5. T. Sireesha and Dr. K. Sreenivasa Ravi published a paper entitled with "**Comparative Assessment on Ramp and Bias Voltage Variations of closed Loop Interferometric Fiber Optic Gyroscope**" in National Conference of Advancements in Embedded Systems and Sensor Networks (NCAESSN), May 2015, KL University.
6. T. Sireesha and Dr. K. Sreenivasa Ravi published a paper entitled with "**Design of an Autonomous Vehicle for Precision Agriculture using Sensor Technology**" in National Conference of Advancements in Embedded Systems and Sensor Networks (NCAESSN), May 2015, KL University.
7. T. Sireesha, M. S. V. Vara Prasad, and Dr. K. Krishna Murthy published a paper entitled with "**A Numerical Simulation of Optical Receiver Performance of WDM Signals in a PON**" in Conference of Recent Trends in Electronics and Communication Systems (RTECS), March 2014, KL University.

Seminars

1. Attended "A Seminar on BROADCAST IN IP WORLD" on 3 November 2018, PSCMRCET, Vijayawada
2. Attended "ENGINEERING EDUCATION & RESEARCH SEMINAR ON NI LABS" on 18 November 2016 at HOTEL SINDOORI, Brodipet, Guntur.
3. Attended to "The Complimentary Seminar on MATLAB & SIMULINK FOR ENGINEERING EDUCATION" at Gateway Hotel, Vijayawada on 05 February 2016.

Workshops

1. Attended "A Workshop on GIS DAY 2018" on 14 November 2018, PSCMR CET, Vijayawada
2. Attended "A Two day IEEE Workshop on LEADERSHIP OPPURTUNITIES FOR PROFESSIONAL WOMEN" from 27 to 28 October 2018 AT EFFTRONICS SYSTEMS PVT. LTD.
3. Attended "A Workshop on DIGITAL TWINS" on 12 OCTOBER 2018, PSCMR CET, VIJAYAWADA
4. Attended "A 3 day National Workshop on 5G TECHNOLOGIES, IMPACT ON OEM-ODM-CSP, INDIA'S DEPLOYMENTS & NB- IOT 2018" with AT. KISHORE, UTL TECHNOLOGIES", from 24 to 26 August 2018, PSCMR CET, Vijayawada
5. Attended "A 5 day Workshop on INTERNET OF THINGS (IOT)" from 24 to 29 July 2017, PSCMR CET, Vijayawada
6. Attended "A IEEE National Workshop on PATTERN BASED COMPETENCE BUILDING" by Ravi Shankar, PSCMR CET on 29 November 2017.
7. Attended "A Workshop on FEVICRYL" from 20 to 23 December 2017, PSCMR CET, Vijayawada
8. Attended "A Workshop on ARC GIS WEB MAPS" at PSCMR CET. on 16 November 2016.
9. Attended "A 3 day IEEE Workshop on ARDUINO AND ITS APPLICATIONS" from 5 to 7 January 2017, PSCMR CET, Vijayawada
10. Attended "A 2 day Workshop on INTRODUCTION TO ROBOTICS" at Sri Chandrasekharendra Saraswathi Viswa Maha Vidyalaya (SCSVMV) University, Kanchipuram in association with e-Yantra from 22 to 23 February 2016.
11. Participated "A 3 day Workshop on PCB DESIGN" from 10 to 12 September 2015, PSCMR CET, Vijayawada

Guest Lectures

1. Attended "A Guest lecture on Domain Overview for Embedded Systems" by Ravi Tammana, at PSCMR CET on 09 February 2018
2. Attended "A Guest lecture on Opportunities in VLSI Industry" by Basavaiah, Technical Leader, ARICENT TECHNOLOGIES, Bengaluru, at PSCMR CET on 28 December 2017
3. Attended "A Guest lecture on Introduction to Speech Processing and Its Applications" by Dr. K. Sreenivasa Rao Associate Professor, Indian Institute of Technology Kharagpur, at PSCMR CET on 01 September 2017.
4. Attended "A Guest Lecture on Present Scenario on Electronics in Industry" by P. Kalee Prasad, Senior Engineer (Assistant), Doordharshan, Vijayawada, at PSCMR CET on 27 December 2016.
5. Attended "A Guest Lecture on TRENDS AND CHALLENGES IN VLSI DESIGN" at PSCMR CET on 01 September 2015.

Training Programs

1. Attended "AP SKILL DEVELOPMENT Program on MATLAB & ITS APPLICATIONS" on 29 October 2018, PSCMR CET, Vijayawada.
2. Attended Training Program on "MOODLE" ON 28 JULY 2018, PSCMR CET, Vijayawada
3. Attended Task based Training (TBT-2016) Program on "Teacher Training Through the e-Yantra Lab Setup Initiative" from IIT Bombay on June 2016.
4. Attended Training Program on "HAM RADIO" at PSCMR CET on 31 July 2016.

FDPs

1. Attended "A 5 day FDP on INTERNET OF THINGS (IOT)" from 23 to 27 October 2018, PSCMR CET, Vijayawada
2. Attended "A 5 day FDP on INSTRUCTIONAL DESIGN & DELIVERY" from 4 to 8 June 2018, PSCMR CET, Vijayawada
3. Attended "APSSDC FDP on Android based Applications" at PSCMR CET. (gotoWebinar Website) from 31 August 2016 to 5 September 2016.
4. Attended "APSSDC FDP on Android based Applications" at VVIT, Nambur, Guntur District on 6 September 2016.
5. Attended "A 5 day FDP on MAGICAL TEACHER" from 9 to 13 November 2016, PSCMR CET, Vijayawada
6. Attended "A 2 day FDP on LATEX" at PSCMR CET on Jan/Feb 2016.

Events organized/ Conducted

1. Organised "A 5 day Workshop on PCB DESIGN" from 11 to 15 September 2018, PSCMR CET, Vijayawada
2. Organised "A 5 day Workshop on PCB DESIGN" from 21 to 26 August 2017, PSCMR CET, Vijayawada
3. Organized valedictory and Inauguration Function "e-Yantra Lab Setup Initiative (eLSI)" at PSCMR CET on 29 July 2016.
4. Organised "A 5 day Workshop on PCB DESIGN" from 25 to 30 November 2016, PSCMR CET, Vijayawada

Resource person

1. Acted as Judge for paper presentation of Polytechnic students in the occasion of "JIGNASA-2018" conducted by PSCMR CET on 10 Jan 2018.
2. Acted as a Resource Person for one day for "A 3 day Workshop on SCILAB APPLICATIONS IN ELECTRONICS AND COMMUNICATION" from 28 to 30 June 2018, PSCMR CET, VIJAYAWADA
3. Acted as a Resource Person for one day for "A 3 day Workshop on SCILAB APPLICATIONS IN ELECTRONICS AND COMMUNICATION" at PSCMR CET from 29 June 2017 to 01 July 2017.
4. Acted as a Resource Person for one day for "A Workshop on SCILAB AND ITS APPLICATIONS" at PSCMR CET from 5 to 10 December 2016.
5. Acted as a Resource Person for "A 5 day Workshop on Hands-on Experience with HFSS" from 25 to 30 November 2016, PSCMR CET, Vijayawada
6. Acted as a Resource Person for one day for "A Workshop on MATLAB AND ITS APPLICATIONS" at PSCMR CET from 21 to 30 September 2015.

Other Events Attended/ Participated

1. Attended to an Inauguration Function of "H-BOTS INCUBATION CENTRE (H-LABS)" at PSCMR CET on 01 September 2018.
2. Attended "Alumni Meet-2", PSCMR CET on 26 November 2017.
3. Attended to an Inauguration Function of "EURTH TECHTRONICS" at PSCMR CET on 22 January 2018.
4. "An Interactive Session on Women Empowerment" by Smt. D. Purandareswari at PSCMR CET on 27 January 2018.
5. Attended to an Inauguration Function of "IOT Lab by APPSDC" at PSCMR CET on 09 March 2018.
6. Attended an "INTERACTIVE SESSION ON CAREER DEVELOPMENT" by oomen cherian, Vice President Oracle on 12 November 2016, PSCMR CET, Vijayawada

7. Attended "A Motivational Talk on BE THE CHANGE" by G.V. Rao at PSCMR CET on 29 December 2016.
8. Participated in the occasion of "JIGNASA-2017 A NATION LEVEL TECHNICAL SYMPOSIUM" at PSCMR CET on 11 January 2017.
9. Participated in "PROJECT EXPO" from 11 to 12 January 2016, PSCMR CET, Vijayawada
10. Attended "Global Entrepreneurship Summit 2016", at IIT Kharagpur from 8 to 10 January 2016

Funding Projects Handled (If any – Please provide the details of the project)

1. Project Title: **"Autonomous Vehicles Using IOT & Computer Vision"**
Submitted project idea proposal was shortlisted to **e-Yantra Ideas Competition-2019.**
2. Project Title: **"Footstep Electricity"**
Submitted project idea proposal was shortlisted to **e-Yantra Ideas Competition-2018.**
3. Project Title: **"Wireless Energy Transfer (WET)"**
Submitted project idea proposal to Texas Instruments in collaboration with Department of Science and Technology (DST) proudly announce the **DST & Texas Instruments India Innovation Challenge Design Contest-2017** anchored by NSRCEL, Indian Institute of Management, Bangalore (IIMB) and supported by MyGov in November 2017.
4. Project Title: **"Adaptive Health Monitoring System using IOT"**
Submitted project idea proposal to **"Intel Higher Education Challenge – 2017"** in Cyber Physical Systems conducted by FICE Lab in 1st September 2017.
5. Submitted a **proposal to AICTE** for Seminar/Workshop titled "A 5-Day workshop on PCB Design"
6. Participated in "e-Yantra – IIT Bombay Ideas Competition Challenge-2016" from 8 to 13 August 2016.

Honorariums received

1. On the Occasion of **"Engineers Day Celebrations-2018"** honorariums received from PSCMR CET on 15 September 2018.
2. On the eve of **"Annual Day Celebrations-2018"** honorariums received from PSCMR CET on 24 March 2018.



Signature